

U.S. Department of Commerce, Patent and Trademark		Atty. Docket No.	Application No.
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> MAY 29 2003 (Use several sheets if necessary)		SNDK.026US3	08/908,265
		Applicant(s)	
		Auclair et al.	
		Filing Date	Group
		August 7, 1997	2133

## U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
EM	4,964,079 ✓	10/16/90	Devin			
EM	5,394,359 ✓	2/28/95	Kowalski			
EM	5,523,972 ✓	6/4/96	Rashid et al.			
EM	5,532,962 ✓	7/2/96	Auclair et al.			
EM	5,583,812 ✓	12/10/96	Harari			
EM	5,689,465 ✓	11/97	Sukegawa et al.			
EM	5,751,639 ✓	5/98	Ohsawa			
EM	5,761,125 ✓	6/2/98	Himeno			
EM	5,909,449 ✓	6/1/99	So et al.			

## U.S. Published Patent Application Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

## Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No
EM	WO9012400	10/18/99	WIPO					

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EM	Lee, E.K.F., et al., "Error Correction Technique for Multivalued MOS Memory." <i>Electronic Letters</i> , Vol. 27, No. 15 (July 18, 1991) pp. 1321-1323.

 Examiner Emmanuel L. Moise Date Considered 9/2/03

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.